	Notice of References Cited	Application/Control No. 10/697,164		Applicant(s)/Patent Under Reexamination LIM ET AL.	
		Examiner Ben C. Wang		Art Unit 2196	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2004/0019873 a1	01-2004	Pochayevets et al.	717/101
*	B	US-2002/0138244 a1	09-2002	Meyer, Steven J.	703/14
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Dalton Project, http://www.cs.ucr.edu/~dalton/i8051/i8051syn - Synthesizable VHDL Model of 8051/testall.hex and Simulation, 02/20/2001, Dept. of Computer Science, University of California
	V	D. Wallner, http://www.opencores.org/cvsweb.shtml/ax8/sw/hex2rom.cpp - Revision 1.4, 10/27/2002, OPENCORES.ORG
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.